-B Drafts — স্বাধ্যমিক কিন্তু কিন্ত				
*	Bits	Search Text	DELE	Pine Stan
	1701	(phase adj change) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/22 11:18
:	292	(phase adj change) with memory and "257"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/22 15:56
	14	pram and "257"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/22 11:14
	7848	257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.	US-PGPUB; USPAT: EPO; JPO; DERWENT;	2004/09/22 11:19
		((phase ad) Change) with memory) and (23/72,4,104,134,200,203,400,411,330,13100123 130,12100120 130,12100120 130,131000120 130,131000120 130,1310000000000000000000000000000000	US-PGPUB; USPAT: EPO; JPO; DERWENT;	2004/09/22 15:56
		and transistor and (source or drain)	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2005/01/21 15:22
		(((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.)) not (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.)	JPO; DERWENT;	2005/01/2 15:23
		(((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.)) not (((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.) and transistor and (source or drain))	JPO: DERWENT;	2005/01/2: 15:23
	292	(phase adj change) with memory and *257*/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/22 15:56
3	105	((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/2 15:57
	199	((phase adj change) with memory and "257"/\$.ccls.) not (((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.))	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/2 15:57
2		"20020036931".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2004/09/2 16:21
		((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.) and transistor and (source or drain) and @pd>"20040923"	US-PGPUB; USPAT; EPO; JPO; DERWENT;	2005/01/2 15:22
		(((phase_adj_change)_with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.)) not ((257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.) and (pd>*20040923*	JPO: DERWENT;	2005/01/2 15:23
		(((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.)) not (((phase adj change) with memory) and (257/2,4,184,194,200,203,400,421,530,734.ccls. 438/131.ccls. 365/145,175.ccls.) and transistor and (source or drain)) and %pd>*20040923*	JPO; DERWENT;	2005/01/2 15:23
•		\$13 \$14 \$15	US-PGPUB; USPAT: EPO; JPO: DERWENT;	2005/01/2 15:23

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